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Docket No.: 9323.051.00  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
JEONG, In Kwon

Customer No.: 30827

Application No.: 09/931,695

Confirmation No.: 3412

Filed: August 16, 2001

Art Unit: 1753

For: SYSTEM AND METHOD FOR PROCESSING  
SEMICONDUCTOR WAFERS USING  
DIFFERENT WAFER PROCESSES

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Examiner: Phasge, Arun

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE REGARDING DRAWINGS**

Dear Sir:

In response to the Notice Regarding Drawings mailed July 7, 2005, Applicant respectfully submits:

1. Replacement Drawings – Figures 1-15 (15 pp.)

In light of the foregoing, this application is deemed to be in proper condition so that it can be passed to issuance.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete

the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: August 4, 2005

Respectfully submitted,

By 

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